



Mirabilis Design produces the first Internet-of-Things modeling solution for early and accurate architecture studies.

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With Visual IoT Modeler, Mirabilis Design enables vendors of semiconductors, smart devices and network operators to explore the architecture for performance, power, and network bandwidth.

Sunnyvale, CA. — November 16th, 2016— Mirabilis Design, the leading provider of system-level modeling and simulation solutions for the systems and semiconductor industry, announced today the release of industry's first VisualSim IoT modeling library.

Visual IoT Modeler enables

- Semiconductor vendors to select the right processor, bus topology, and memory capacity.
- Smart device vendors to explore the selection of sensor chips, cognitive radio communication, battery capacity and the design of smart hub for sensor data processing.
- Network operators to compute the number of routers, servers, and storage devices and also assess network bandwidth and redundancy requirements.

“Battery life, growing feature set and reduced data sizes are becoming the primary requirements for all smart connected devices”, said Deepak Shankar, Founder and CEO of Mirabilis Design Inc. “Using VisualSim IoT Modeler, our consumer electronics customers can now get accurate metrics and overcome the limitation of approximate results of the analytical tools at their disposal. The quick drag-n-drop solution demystifies the relationship between power, latency and bandwidth.”

VisualSim IoT Modeler can be used in conjunction with the extensive VisualSim modeling library. The new IoT Modeler adds Low-Power Bluetooth, sensors, intelligent hubs, micro-controllers, cognitive radio, six battery types, and enhanced charging/discharging circuits. To augment the model, we have provided new reports for battery life, battery efficiency, task latency, bandwidth utilization, processor MIPS, memory throughput, and buffer usage.

The existing VisualSim modeling libraries comprise:

- Interfaces: PCIe, PCI, Ethernet, Gigabit Ethernet, AFDX, TT Ethernet, CAN and FlexRay
- SoC buses: AHB, APB, AXI and CoreConnect
- Hardware components: Processors, DRAM, Cache and DMA
- Software: RTOS, Schedulers, Software Task generators



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Availability

VisualSim IoT Modeler is available now as an add-on to VisualSim Architect 16.2. VisualSim Architect is used extensively in the electronics industry to design products ranging from aircraft avionics to adventure cameras; and processor to safety critical systems. VisualSim IoT Modeler is supported on Windows, Linux, MAC OS, and all other forms of UNIX.

About Mirabilis Design

Mirabilis Design is a Silicon Valley company, providing software solutions to identify and eliminate risk in the product specification; accurately predict the human and time resources required to develop the product; and improve communication between diverse engineering teams. VisualSim Architect is a system-level modeling, simulation, and analysis environment using a complete set of libraries and application templates that significantly improve model construction and analysis time. The environment enables designers to rapidly converge to a design which meets a diverse set of interdependent time- and power requirements. It is optimally used very early in the design process in parallel with (and as an aid to) the development of the product's written specification and long before an implementation (for example, RTL, software code, or schematic) of that product can even be started.

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